

**Amendments to the Specification:**

Please amend the paragraph beginning at page 7, line 10 as follows:

The debug client function 120 contains either an independent or dependent debugger client. The preferred embodiment is a simple dependent client, with each debugging session information downloaded (the FPGA is "programmed") by the debugger server 240, much like the model of physically connected leads from external buses to a logic analyzer, then setting a trigger point and observing the system. Upon trigger, the system server 240 will capture selected information. This same concept is embodied in the independent debugger client with the addition that one may wish to set a bus or signal to a ~~new~~ known value, either before the observation period or as a result of an observed action. This same process could be used to hold part or all of an ASIC's clocks in a known state or to hold part or all of an ASIC's functional areas in reset modes.